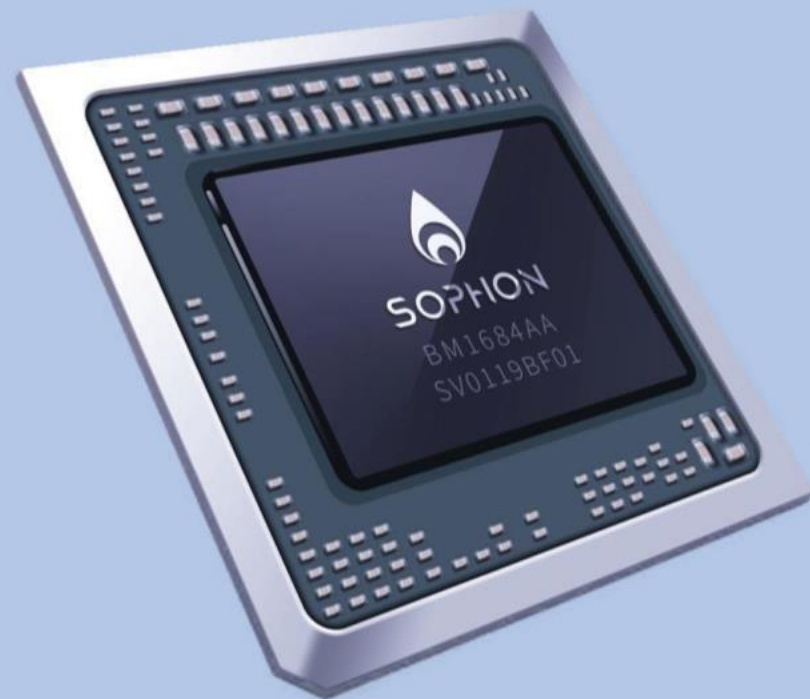


SOPHON Intelligent Computing Chip BM1684

SOPHON



BM1684 Specification



1 Independently Developed TPU Architecture
17.6T INT8; 2.2T FP32

2 On-chip high-speed SRAM reaches 32MB
LPDDR4x bandwidth: 68.3GB/s
Maximum capacity: 16GB

3 8-Core ARM A53
Main Frequency 2.3GHz, 42320 DMIPS

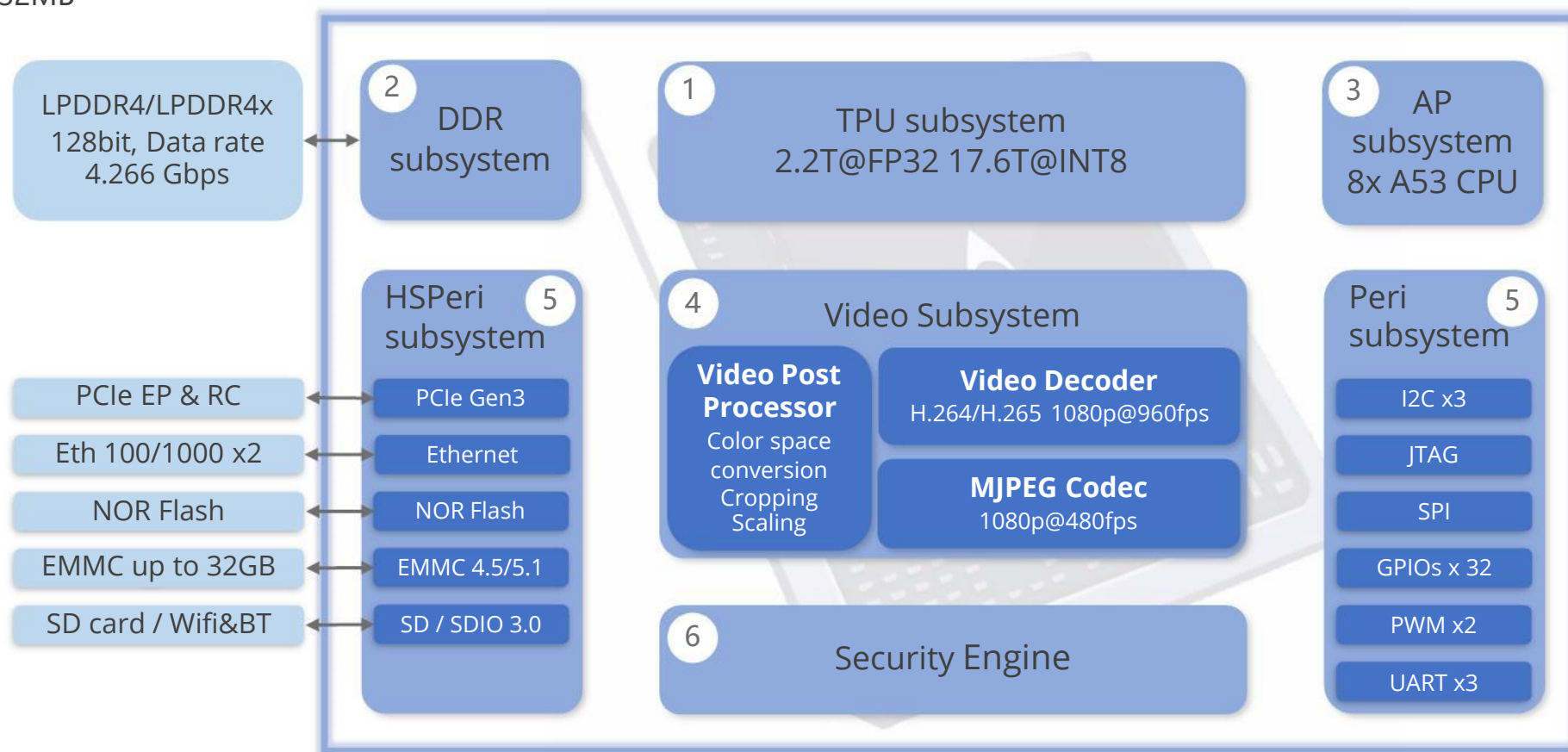
4 Video Decoding 1080p@960fps
Video Encoding 1080p@50fps
MJPEG Codec 480fps 1080P
Video post-processing system

5 Rich I/Os
Dual Gigabit Network Interface
PCIE RC/EP

6 Security engine
Data encryption and decryption
Signature

7 -40~105°C
Typical power consumption 16W

BM1684



SOC

- The 3rd gen. self-developed AI chip with strong acceleration capability in the whole process
- The actual computing power and power consumption are significantly improved compared with competitors

Application

- Multiple industrial and algorithm partners
- Compatible for different CPU and OS

Tools and I/Os

- Support dockerizing and K8S
- Mature and usable SDK and toolchain
- Rapid and convenient algorithm migration

Hardware Type

- Diversified product forms for cloud-edge application
- High density and low energy consumption on the cloud side
- Easy to deploy, maintain and integrate on the edge side

AI Framework

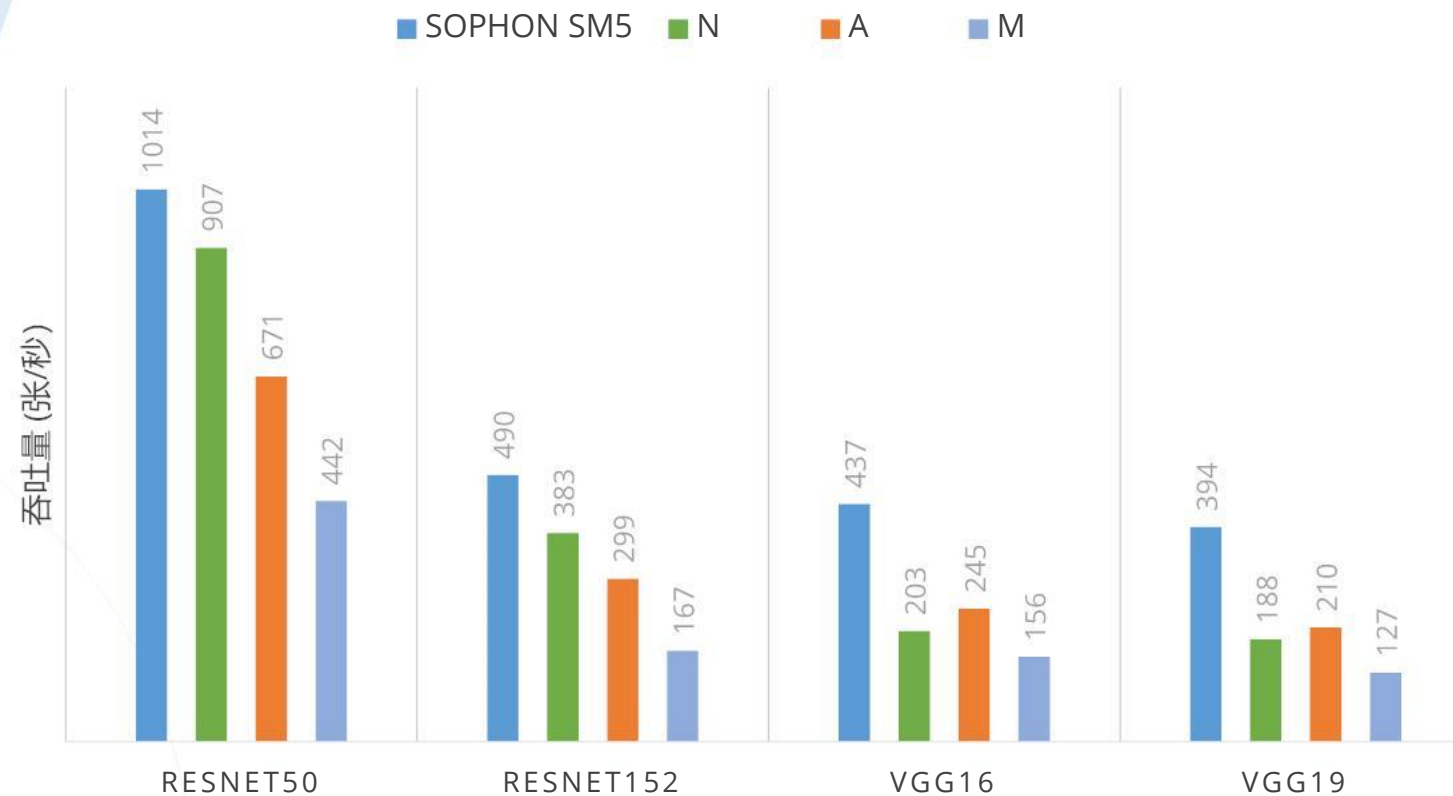
Support mainstream deep learning frameworks:
TensorFlow / PyTorch / Paddle / Caffe / ONNX /
MXNet / Tengine / DarkNet



Competitors Analysis



Measured Performance Analysis (IMG/S)



* Description: N, M and A are the mainstream intelligent computing module platforms in the industry. The data is measured based on INT8 quantized Batch4

VS



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